



1763

APR 04 2003

**TRANSMITTAL OF FORMAL DRAWINGS**

Docket No.  
00750425AA

In Re Application Of: **H. Park, et al.**

Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
09/924,318	08/08/01		Unassigned	1765

Invention: **METHOD OF BUILDING A CMOS STRUCTURE ON THIN SOI WITH SOURCE/DRAIN ELECTRODES FORMED BY IN SITU**

Address to:  
Assistant Commissioner for Patents  
Washington, D.C. 20231

RECEIVED  
APR 8 2003  
TC 1700

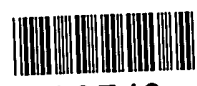
Transmitted herewith are:

4 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

*Signature*

Michael E. Whitham  
Reg No. 32,635  
Whitham, Curtis & Christofferson, P.C.  
11491 Sunset Hills Road, Suite 340  
Reston, Virginia 20190  
703-787-9400



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PATENT TRADEMARK OFFICE

Dated: April 4, 2003

I certify that this document and attached formal drawings are being deposited on \_\_\_\_\_ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

*Signature of Person Mailing Correspondence*

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